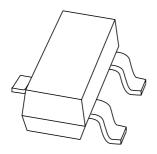
### **DISCRETE SEMICONDUCTORS**

# DATA SHEET



# PMBTA56 PNP general purpose transistor

Product specification Supersedes data of 1999 Apr 09 2004 Jan 09





## PNP general purpose transistor

#### PMBTA56

#### **FEATURES**

• High current (max. 500 mA)

• Low voltage (max. 80 V).

#### **APPLICATIONS**

 General purpose switching and amplification, e.g. telephony and professional communication equipment.

#### **DESCRIPTION**

PNP transistor in a SOT23 plastic package. NPN complement: PMBTA06.

#### **MARKING**

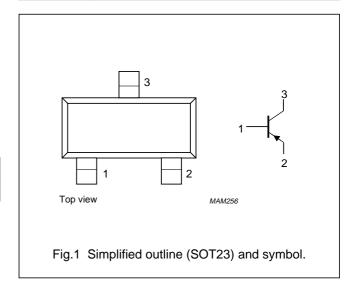
TYPE NUMBER	MARKING CODE <sup>(1)</sup>	
PMBTA56	*2G	

#### Note

\* = p : Made in Hong Kong.
 \* = t : Made in Malaysia.
 \* = W : Made in China.

#### **PINNING**

PIN	DESCRIPTION
1	base
2	emitter
3	collector



#### **ORDERING INFORMATION**

TYPE	PACKAGE				
NUMBER	NAME	NAME DESCRIPTION VERSION			
PMBTA56	_	plastic surface mounted package; 3 leads SOT23			

#### LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter	_	-80	V
V <sub>CEO</sub>	collector-emitter voltage	open base	_	-80	V
V <sub>EBO</sub>	emitter-base voltage	open collector	_	<b>-</b> 5	V
I <sub>C</sub>	collector current (DC)		_	-500	mA
I <sub>CM</sub>	peak collector current		_	-1	Α
I <sub>BM</sub>	peak base current		_	-200	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C; note 1	_	250	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C
T <sub>j</sub>	junction temperature		_	150	°C
T <sub>amb</sub>	operating ambient temperature		-65	+150	°C

#### Note

1. Transistor mounted on an FR4 printed-circuit board.

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# PNP general purpose transistor

PMBTA56

#### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	note 1	500	K/W

#### Note

1. Transistor mounted on an FR4 printed-circuit board.

#### **CHARACTERISTICS**

 $T_{amb}$  = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I <sub>CBO</sub>	collector cut-off current	$I_E = 0; V_{CB} = -80 \text{ V}$	_	-50	nA
I <sub>EBO</sub>	emitter cut-off current	$I_C = 0; V_{EB} = -5 \text{ V}$	_	-50	nA
h <sub>FE</sub>	DC current gain	$I_C = -10 \text{ mA}; V_{CE} = -1 \text{ V}$	100	_	
		$I_C = -100 \text{ mA}; V_{CE} = -1 \text{ V}$	100	_	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_C = -100 \text{ mA}; I_B = -10 \text{ mA}$	_	-250	mV
$V_{BE}$	base-emitter voltage	$I_C = -100 \text{ mA}; V_{CE} = -1 \text{ V}$	_	-1.2	V
f <sub>T</sub>	transition frequency	$I_C = -100 \text{ mA}; V_{CE} = -1 \text{ V};$ f = 100 MHz	50	_	MHz

Philips Semiconductors Product specification

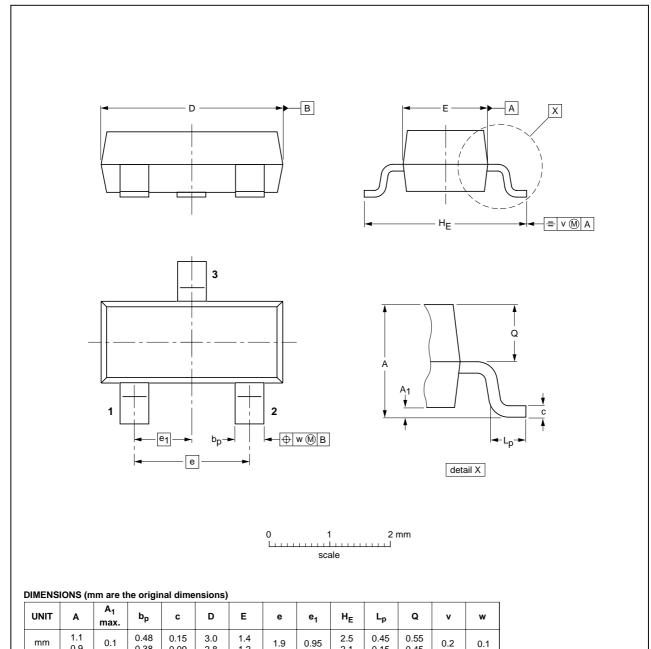
# PNP general purpose transistor

PMBTA56

#### **PACKAGE OUTLINE**

#### Plastic surface mounted package; 3 leads

SOT23



OUTLINE	REFERENCES			EUROPEAN	ICCUE DATE	
VERSION	IEC	JEDEC	EIAJ		PROJECTION ISSUE DATE	
SOT23		TO-236AB				<del>97-02-28</del> 99-09-13

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#### PNP general purpose transistor

PMBTA56

#### **DATA SHEET STATUS**

LEVEL	DATA SHEET STATUS <sup>(1)</sup>	PRODUCT STATUS <sup>(2)(3)</sup>	DEFINITION
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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- 2. The product status of the device(s) described in this data sheet may have changed since this data sheet was published. The latest information is available on the Internet at URL http://www.semiconductors.philips.com.
- 3. For data sheets describing multiple type numbers, the highest-level product status determines the data sheet status.

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Limiting values definition — Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 60134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

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